

08/393677

Atty. Docket No. 950107  
 Date February 24, 1995

THE COMMISSIONER OF PATENTS AND TRADEMARKS  
 Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): Hidehiko KIRA, Masanao FUJII & Naoki ISHIKAWA

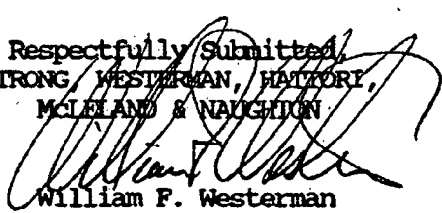
For: METHOD AND SYSTEM FOR FABRICATING A SEMICONDUCTOR DEVICE

- XX Specification 13 pages, Claims 4 pages, Abstract 1 page
- XX Drawings - 6 sheets of drawings.
- XX Declaration and Power of Attorney.
- XX An assignment of the invention to FUJITSU LTD..
- XX An Information Disclosure Statement w/2 Cited References.
- XX A filing fee, calculated as shown below:

	Number Filed	Number Extra	Rate	Basic Fee 730.00
Total Claims	10 - 20	0	x 22.00	
Independent Claims	2 - 3	0	x 75.00	
Multiple Dependent Claims				
Reduction by 1/2 for small entity				-
Fee for recording enclosed Assignment				40.00
TOTAL FEE				770.00

- XX Our check in the amount of \$770.00 for patent application processing fees under 37 CFR 1.16 is enclosed.
- XX The Commissioner is hereby authorized to charge payment for any additional fees associated with this communication or credit any overpayment to Deposit Account No. 01-2340. This paper is filed in triplicate

Respectfully Submitted,  
 ARMSTRONG, WESTERMAN, HATTORI,  
 MCLELAND & NAUGHTON

  
 William F. Westerman  
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